

FIG. 1

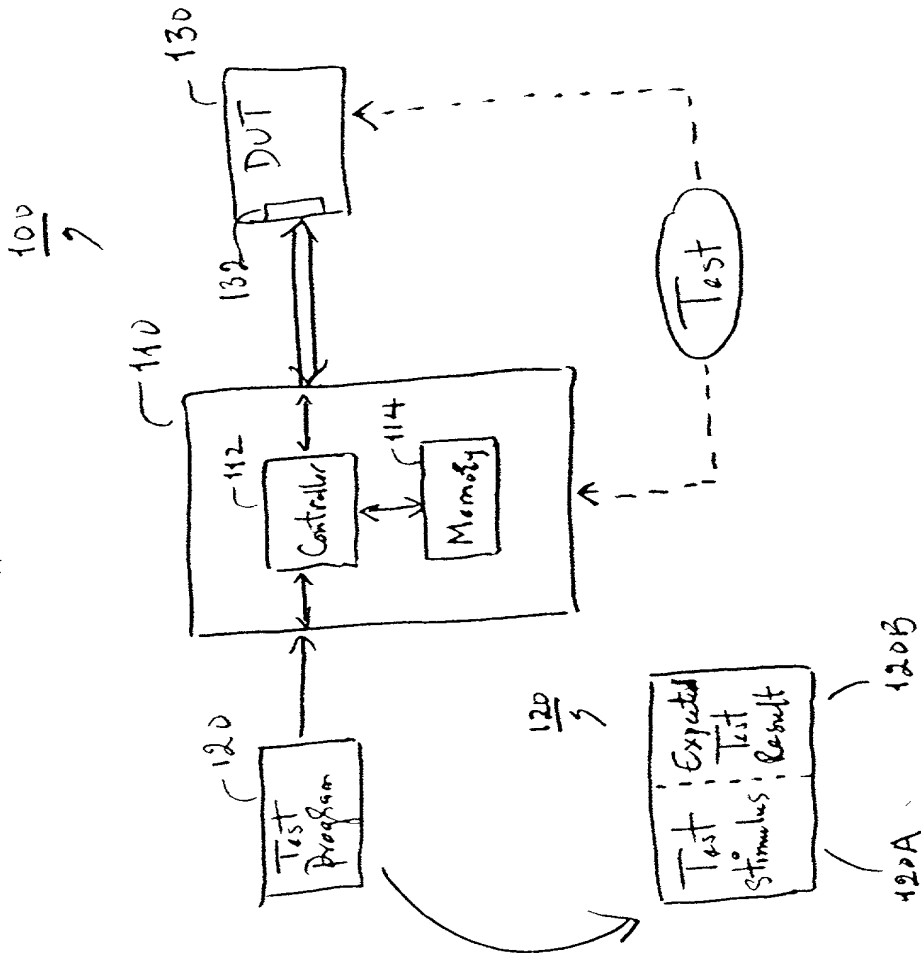


FIG. 2

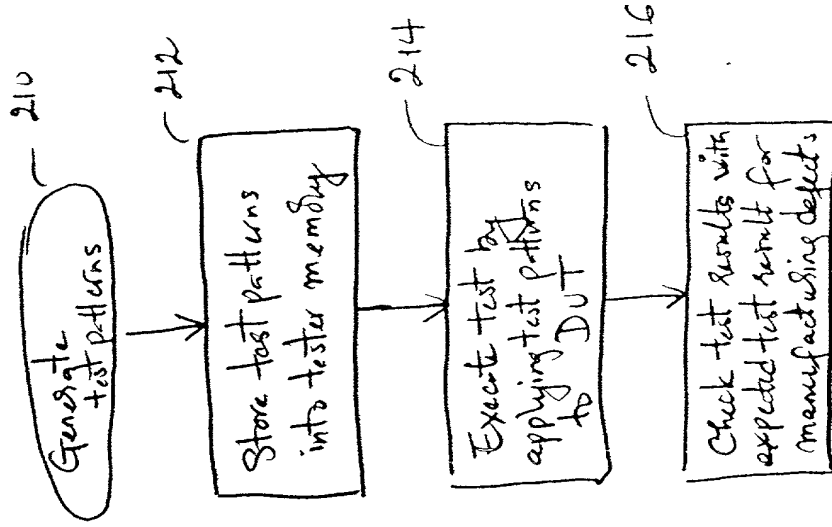


Fig 3. RIT Tool Flow

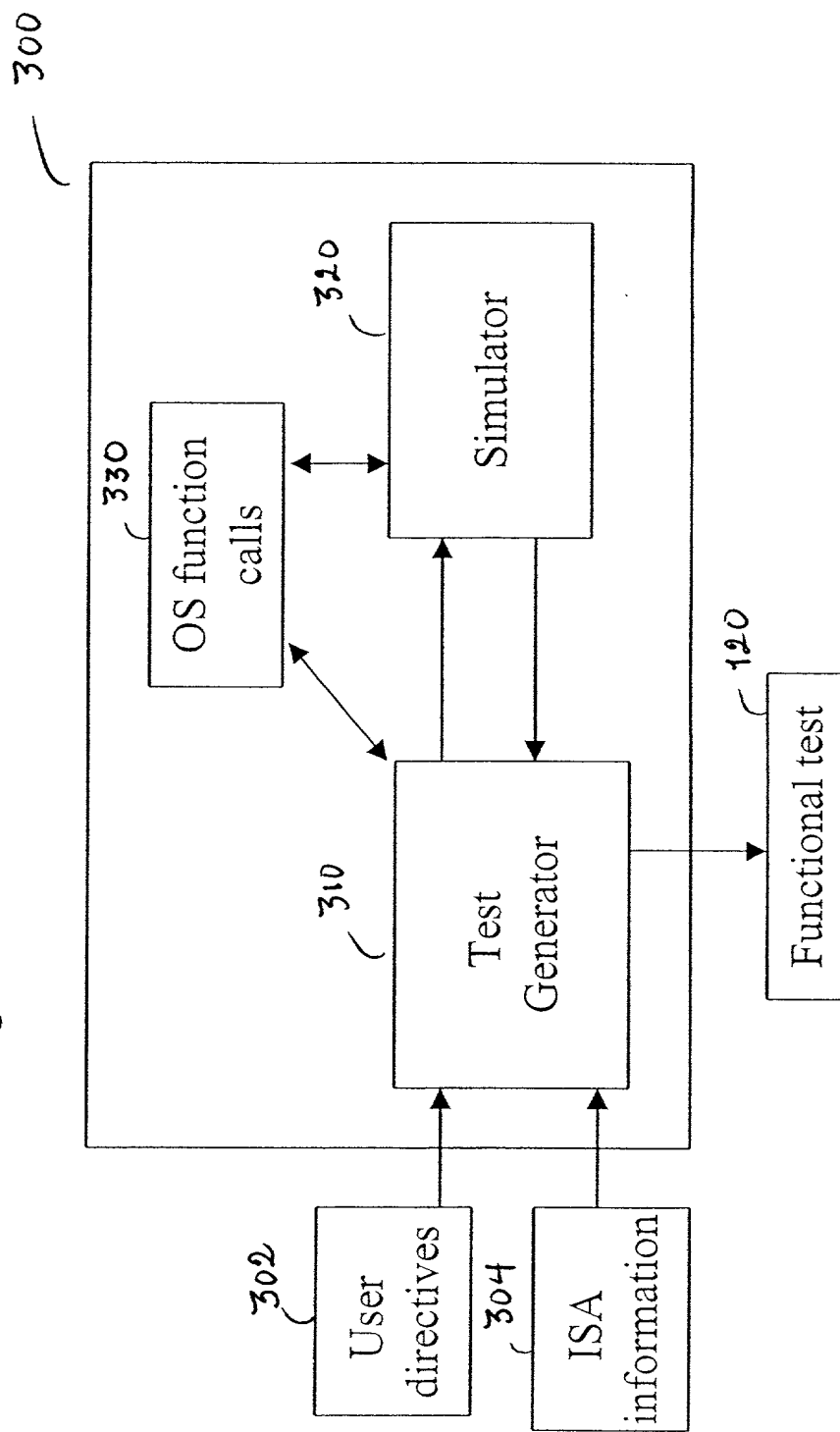




Fig 6. Concept of SBE Generation Tool

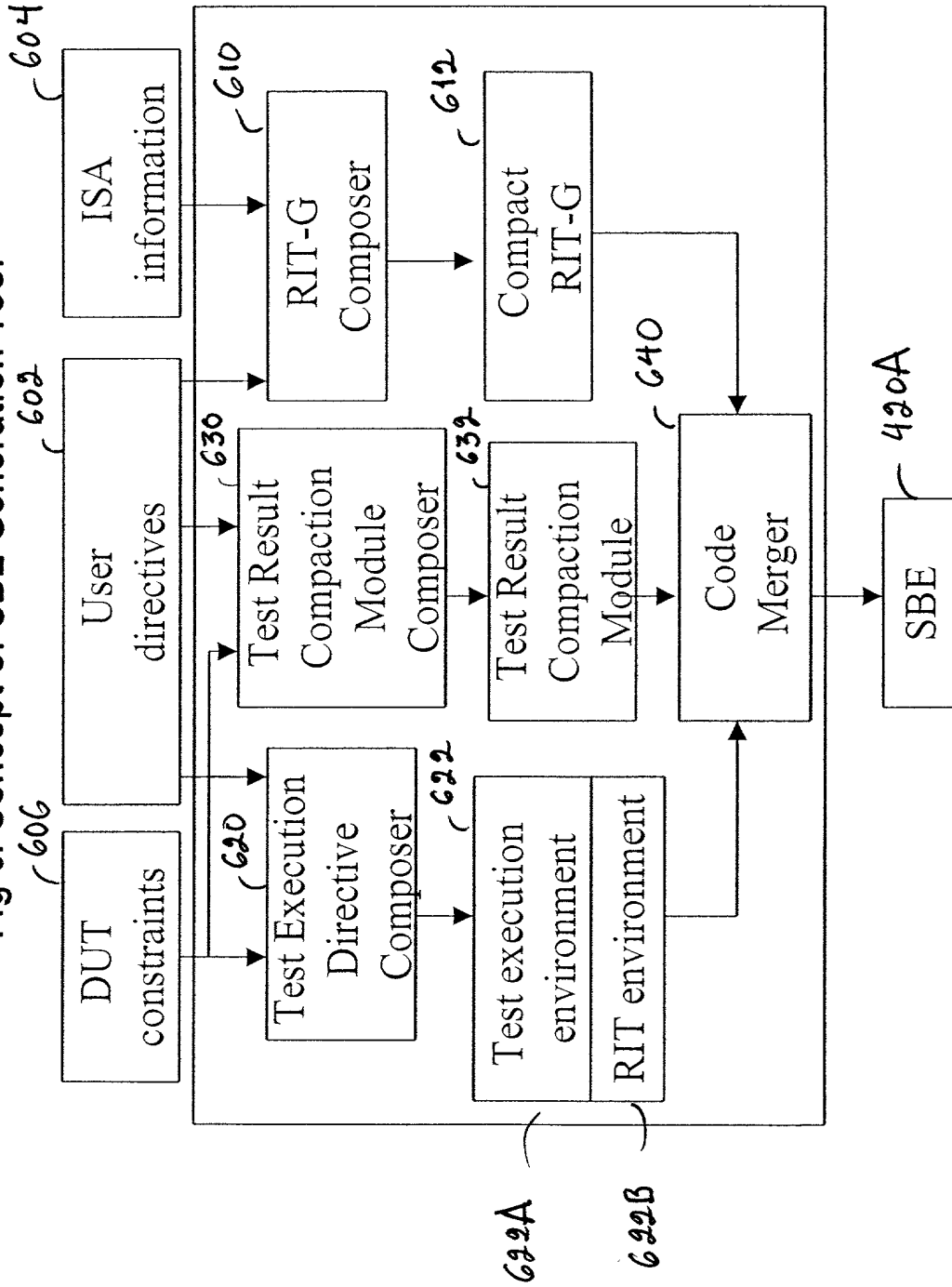
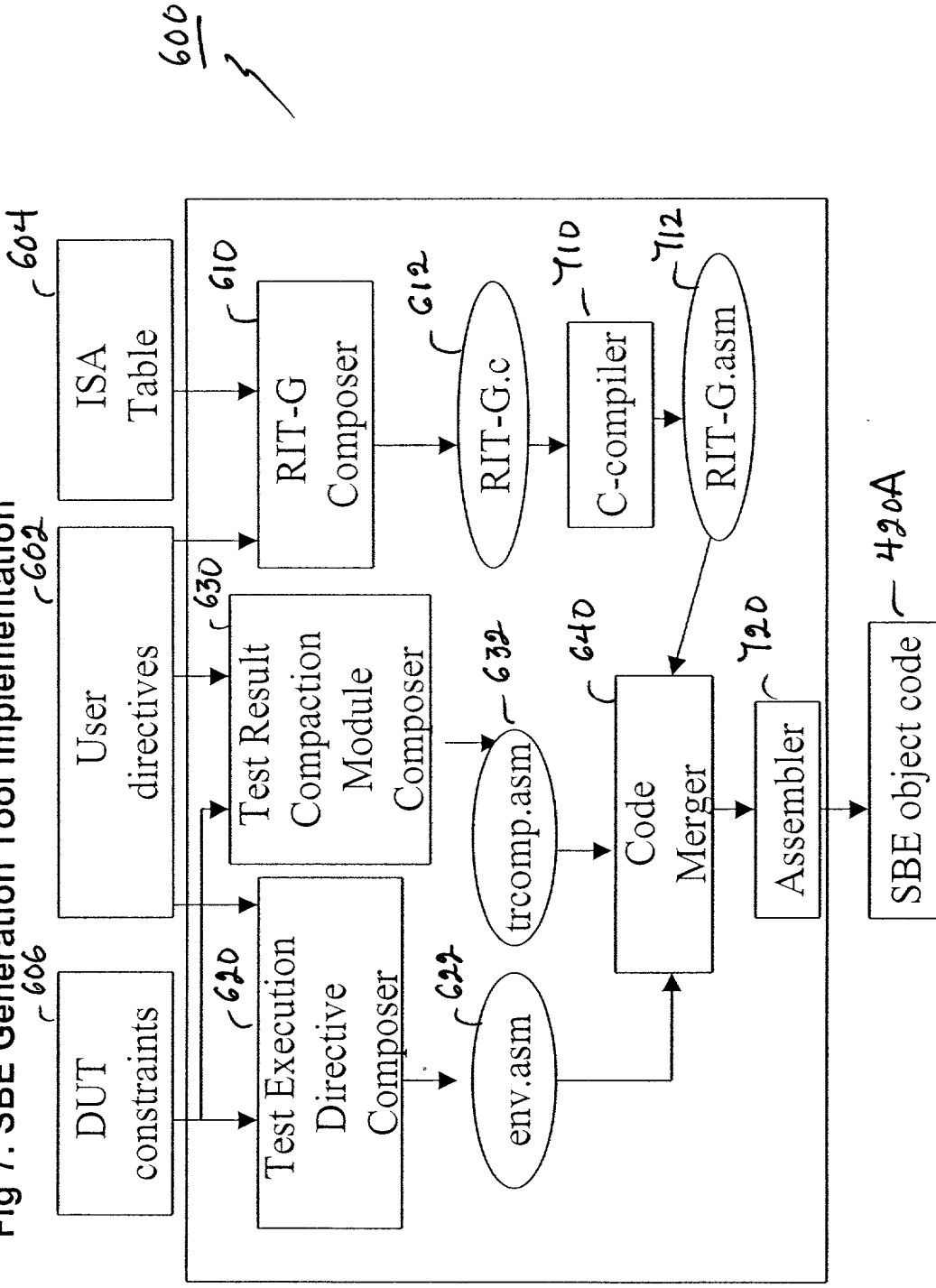


Fig 7. SBE Generation Tool Implementation



**Fig 8: Instruction generation algorithm**

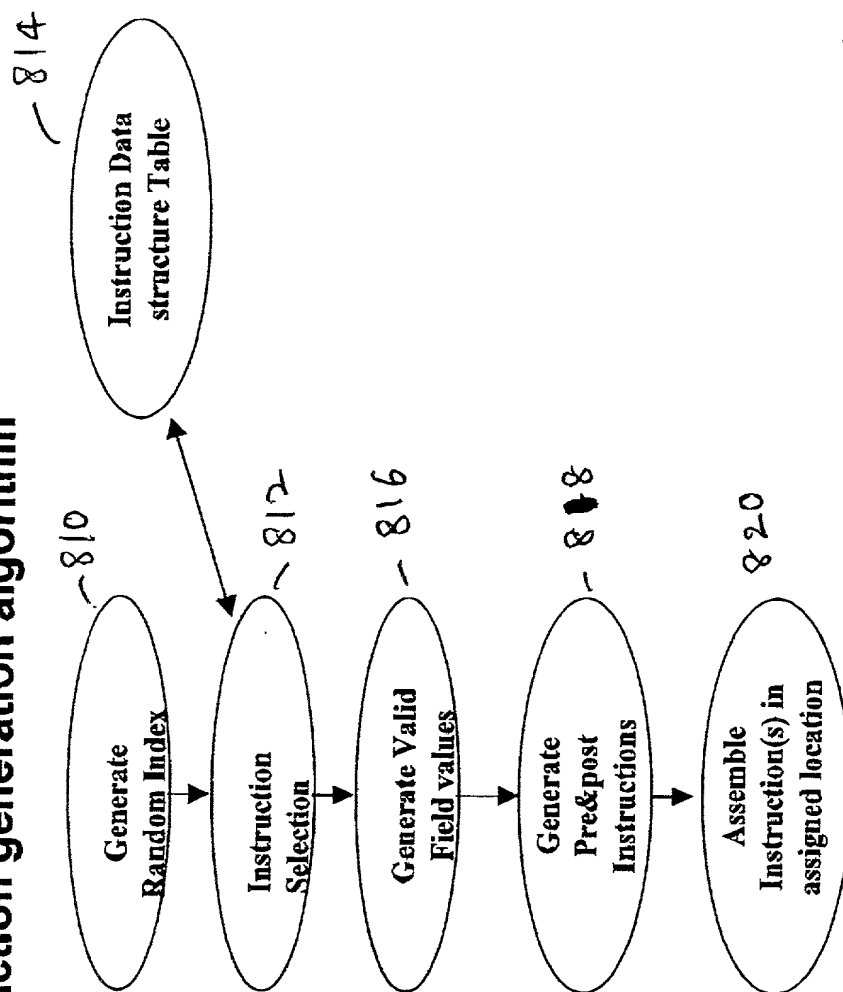


FIG. 9

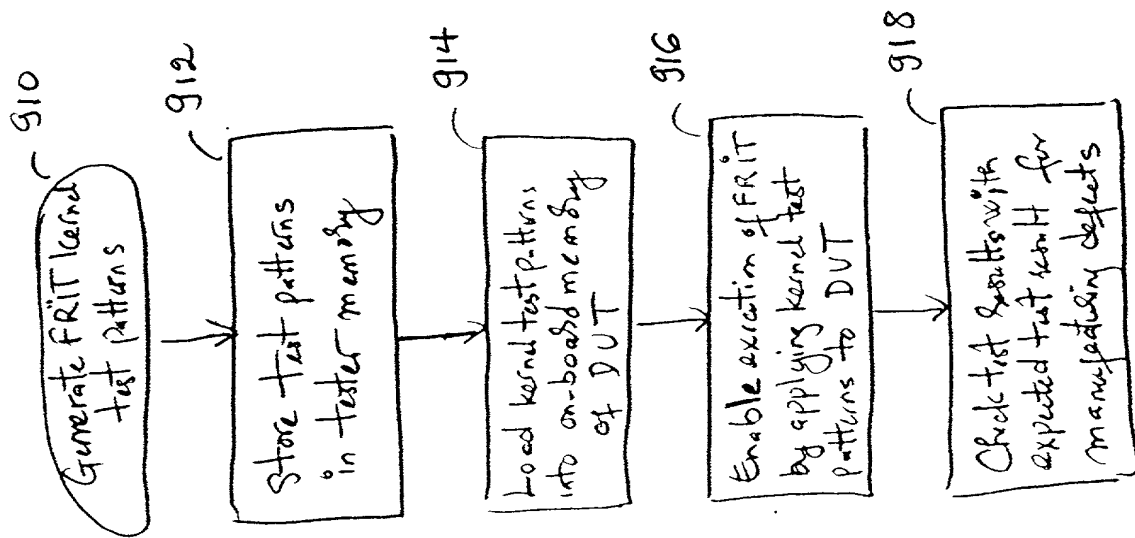


FIG. 10

916  
5

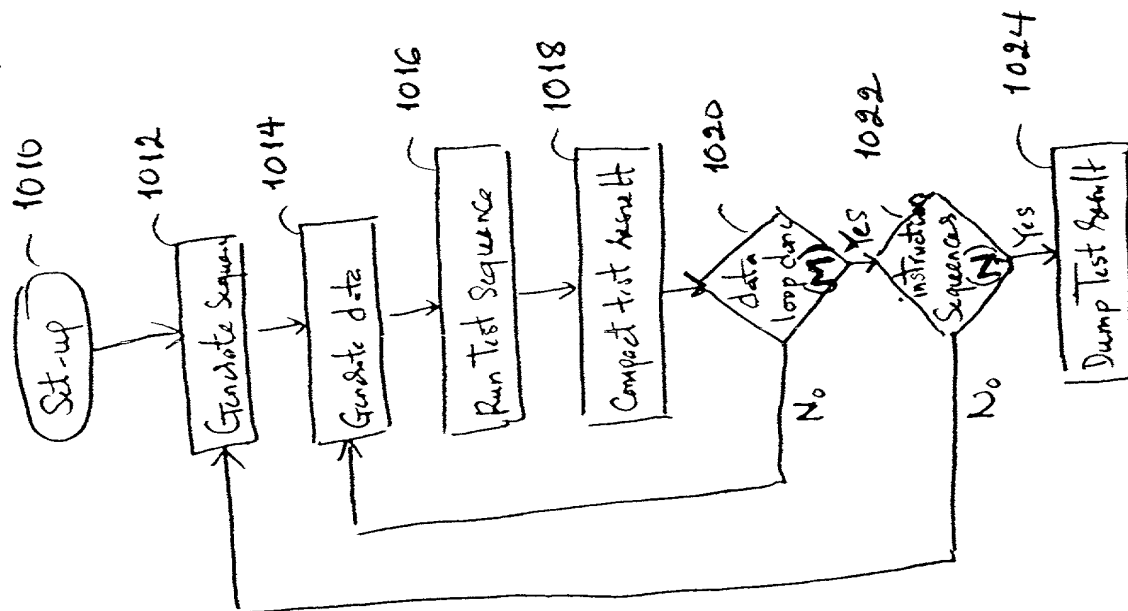


FIG. 11

# FRIT Kernel (FRIT-K) Execution Sequence

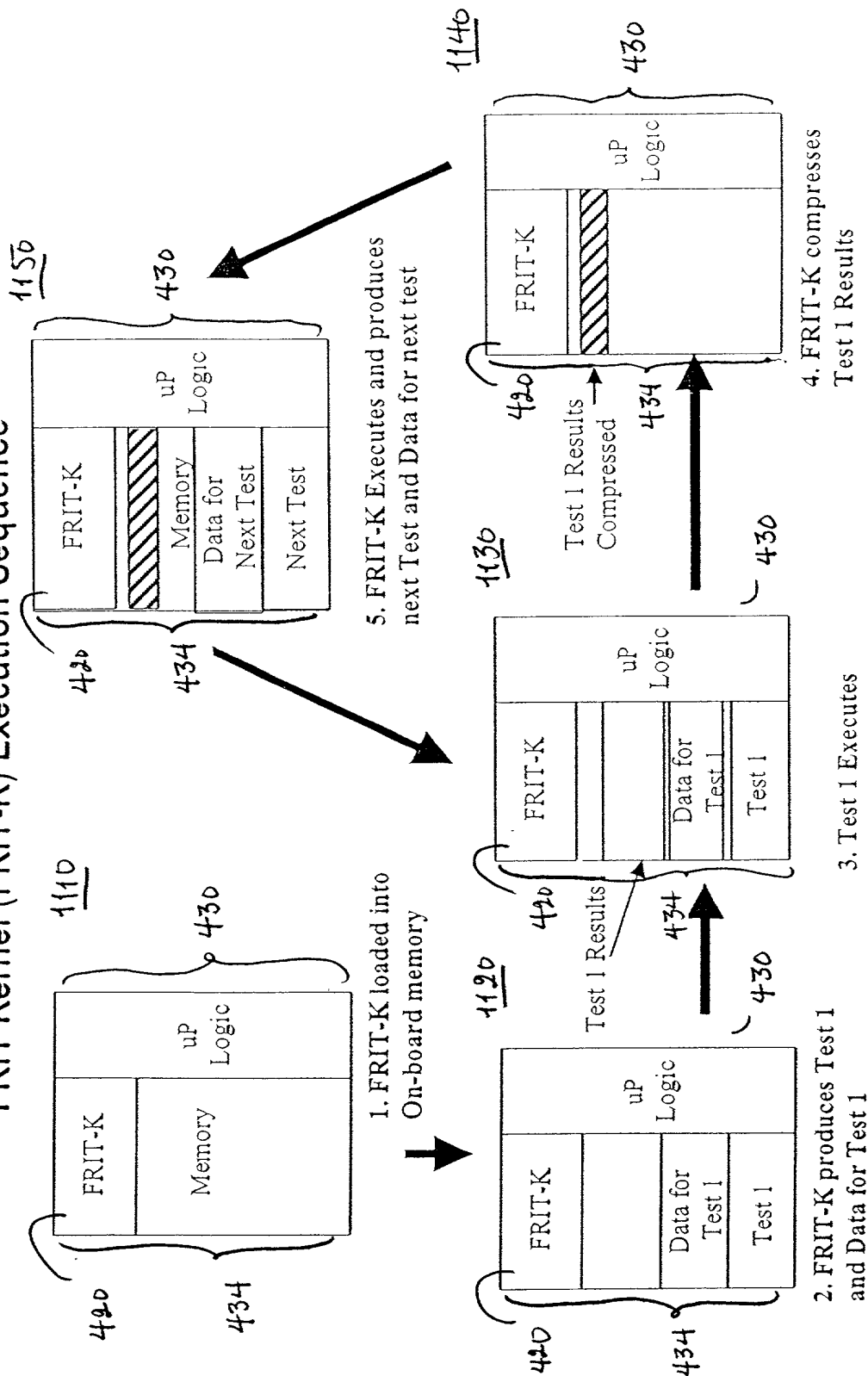




FIG. 12 DUT Memory Image: Beginning of Test

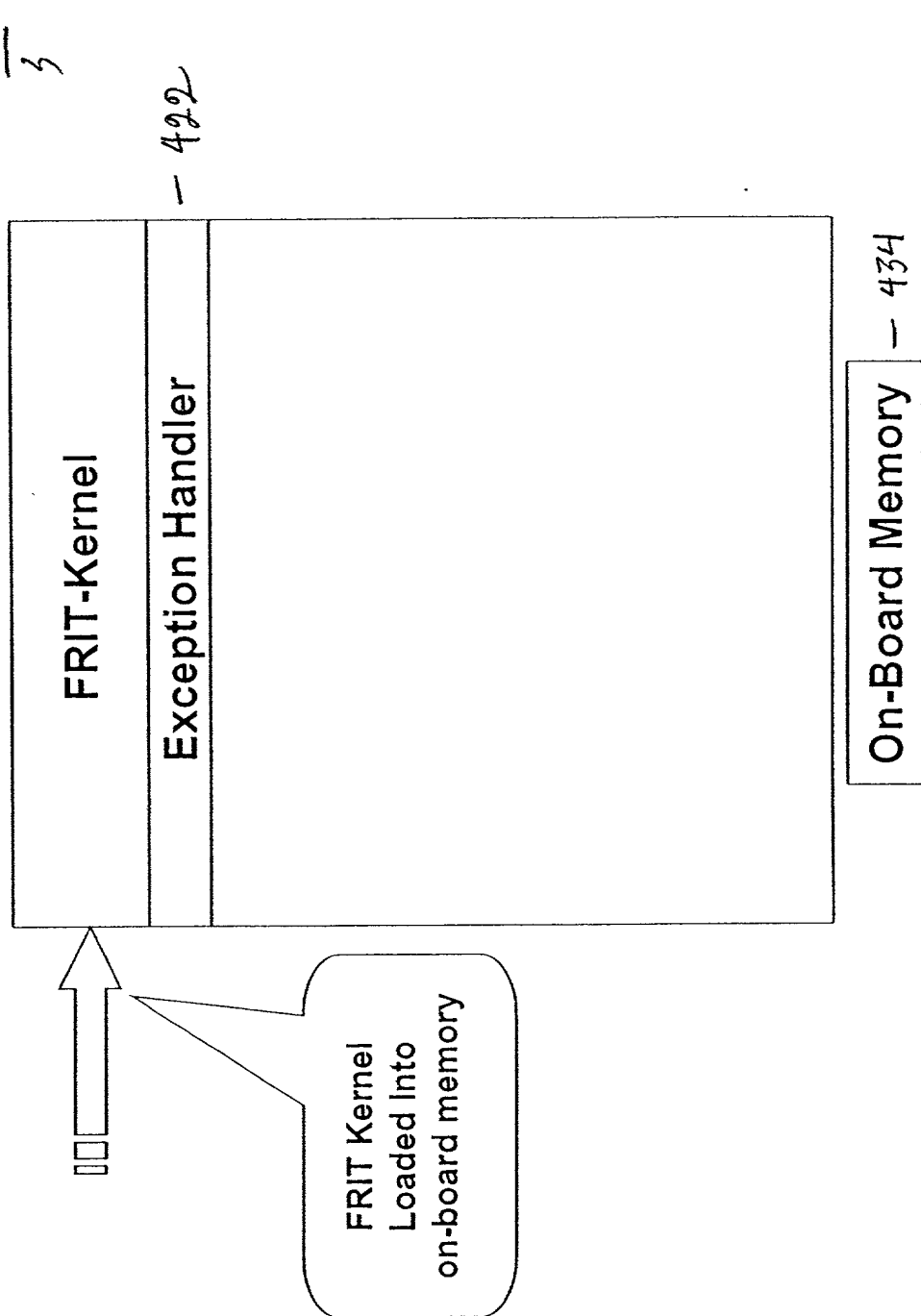


FIG. 13 DUT memory Image: End of Test

420  
5

- 422

FRIT-Kernel
Exception Handler
Test N (binary)
Test N results = = =
Test 3 results
Test 2 results
Test 1 results
On-board Memory

- 434



Results of RIT-Kernel  
Execution dumped  
Out from on-board  
memory to be  
Compared with  
expected  
response